EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	425	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 10:56
S2	64	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or readin or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 10:58
S3	67	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 10:58

		(access or reading or writing))				
S4	67	(chip or (micro near3 chip) or (smart near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 11:13
S5	99	(chip or (micro near3 chip) or (smart near3 card)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 11:14
S6	18	(chip or (micro near3 chip) or (smart near3 card)) same (embed $3) same (device or appliance or (circuit near3 board)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 11:22

		security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))				
S7	789	(chip or (micro near3 chip) or (smart near3 card)) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 11:25
S8	3121	713/193	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:15
S9	576	711/101	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:15
S10	3787	711/103	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:15
S11	1496	711/104	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:15

S12	28	S1 and S8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:16
S13	1	S1 and S9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:16
S14	11	S1 and S10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:16
S15	1	S1 and S11	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:16
S16	53	S7 and S8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:56
S17	2	S7 and S9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:56
S18	3	S7 and S10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:56
S19	0	S7 and S11	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:56

S20	2	"5933628".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 14:25
S21	2	"5933620".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 14:25
S22	2	"5293610".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 14:26
S24	32	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same (protection or security or secure) same ((authorization or authentication) near3 data)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/30 07:41

4/30/2008 3:56:42 PM
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